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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043002	FILING DATE 01/09/2002	CLASS 029	SUBCLASS	GAU 3729	EXAMINER <i>[Signature]</i>
**APPLICANTS: Canova Antonio;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/374,971 08/16/1998					
** FOREIGN APPLICATIONS VERIFIED: WORLD INTELLECTUAL PROPERTY ORGANI 98/36625 02/16/1998 ITALY FI97A000023 02/17/1997 PCT/IT98/00026 08/16/1999					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO N8125 DBP		
TITLE : Process for producing printed circuits and printed circuits thus obtained					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
TERMINAL		Primary Examiner		
DISCLAIMER		PREPARED FOR ISSUE		
Application Examiner				
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FILED WITH:

DISK (CRF)

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